



## SMT1.SOP Serial Signalling Outputs

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BYTE:BIT: FUNCTION

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\*BARNES INTERLOCKING 01

\*MAIN SIGNAL ASPECT

000	0	S517
	1	S516
	2	S515
	3	S514
	4	S513
	5	S512
	6	S511
	7	S510
001	0	S527
	1	S525
	2	S523
	3	S522
	4	S521
	5	S520
	6	S519
	7	S518
002	0	Sd336
	1	Sd334
	2	S533BR
	3	S525BR
	4	S1092
	5	S1090
	6	S550
	7	S529
003	0	
	1	
	2	
	3	
	4	
	5	
	6	Sd551
	7	Sd533

\*BARNES INTERLOCKING 01

\*ROUTES

004	0	R522B(M)
	1	R522A(M)
	2	R521B(M)
	3	R521A(M)
	4	R520A(M)
	5	R519A(M)
	6	R518B(M)
	7	R518A(M)
005	0	R550B(M)
	1	R550A(M)
	2	R529A(M)
	3	R527A(M)
	4	R525B(M)
	5	R525A(M)
	6	R523B(M)
	7	R523A(M)



## SMT1.SOP Serial Signalling Outputs

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BYTE:BIT: FUNCTION

\*BARNES INTERLOCKING 01

006	0	
	1	
	2	
	3	
	4	RKM-KL(M)
	5	R1090C(S)
	6	R1090B(S)
	7	R1090A(S)

\*BARNES INTERLOCKING 01

## \*TRACKS

007	0	THK
	1	THJ
	2	THH
	3	THG
	4	THF
	5	THE
	6	THD
	7	THC
008	0	TJJ
	1	TJH
	2	TJG
	3	TJF
	4	TJE
	5	TJD
	6	TJC
	7	THL
009	0	TKK-1
	1	TKJ
	2	TKH
	3	TKG
	4	TKF
	5	TKE
	6	TJL
	7	TJK
010	0	TMW
	1	TMV
	2	TMU
	3	TMT
	4	TMS
	5	TKL-1
	6	TKL-2
	7	TKK-2
011	0	THG-IP
	1	TKG-IP
	2	TJF-IP
	3	THF-IP
	4	TKL
	5	TKK
	6	TMZ
	7	TMX



## SMT1.SOP Serial Signalling Outputs

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BYTE:BIT: FUNCTION

.....  
\*BARNES INTERLOCKING 01

\*MORTLAKE INTERLOCKING 02

\*MAIN SIGNAL ASPECT

012 0 S557

1 S556

2 S532

3 S541

4 S526

5 S535

6 S533

7 S524

013 0 S539

1 S528

2 S537

3 S551

4 S553

5 S552

6 S555

7 S554

014 0

1 Sd533BR

2 Sd550

3 Sd522

4 SF121

5 SF3

6 S537BR

7 S530

\*MORTLAKE INTERLOCKING 02

\*ROUTES

015 0

1 RHL-HM

2 R532A(M)

3 R541A(M)

4 R526A(M)

5 R535A(M)

6 R524A(M)

7 R533A(M)

\*MORTLAKE INTERLOCKING 02

\*TRACKS

016 0 TND

1 TNC

2 TNB

3 TNA

4 TJM

5 TJN

6 TJP

7 TJQ

017 0 THQ

1 THR

2 THS

3 THT



SMT1.SOP Serial Signalling Outputs

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BYTE:BIT: FUNCTION

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\*BARNES INTERLOCKING 01

	4	THU
	5	THV
	6	THW
	7	THX
018	0	TKR
	1	TKQ
	2	TKP
	3	TKN
	4	TKM
	5	THM
	6	THN
	7	THP
019	0	TdNE
	1	TdJL
	2	TdKL
	3	TdMZ
	4	TKV
	5	TKU
	6	TKT
	7	TKS